Fig. 1A

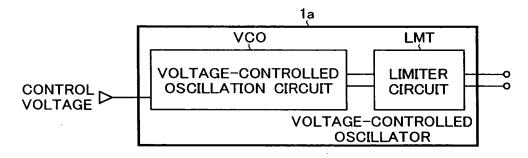
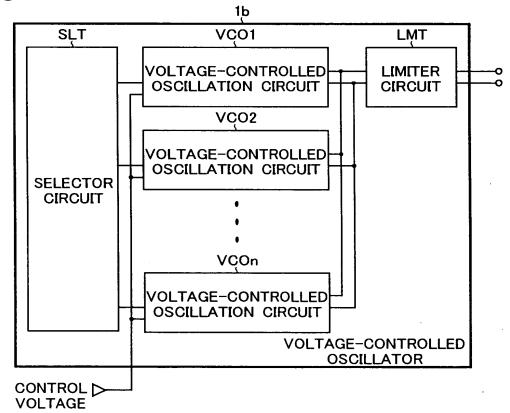
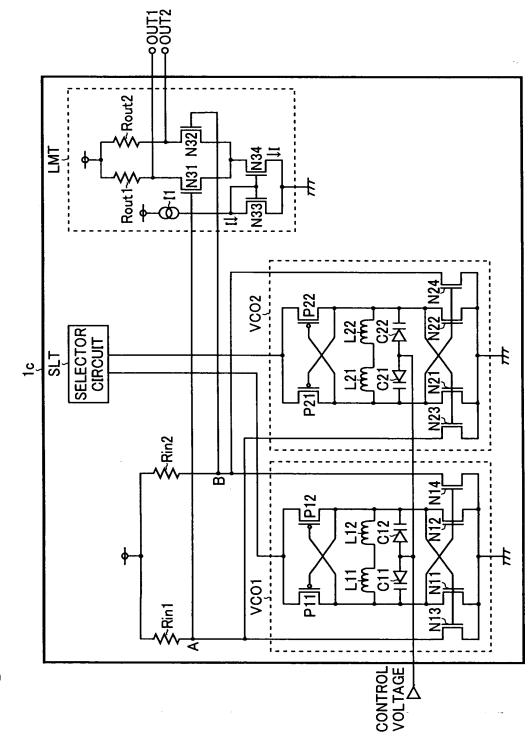


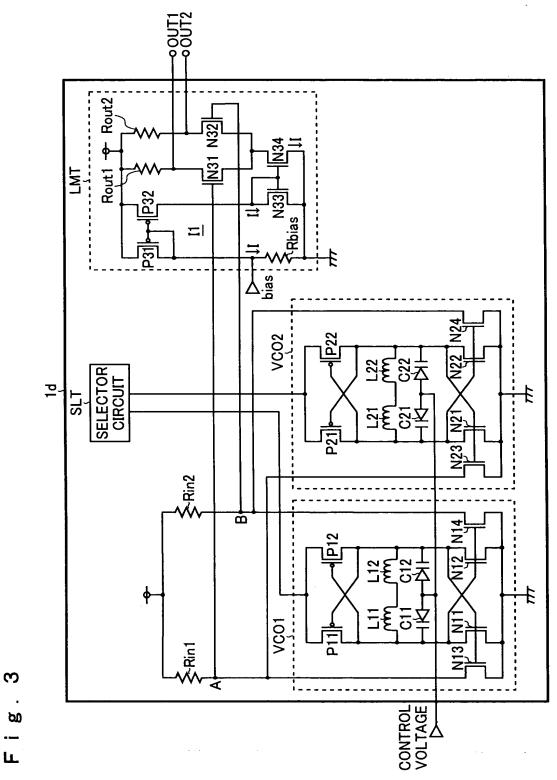
Fig. 1B





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Fig. 4A

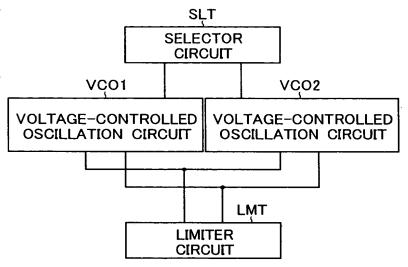


Fig. 4B

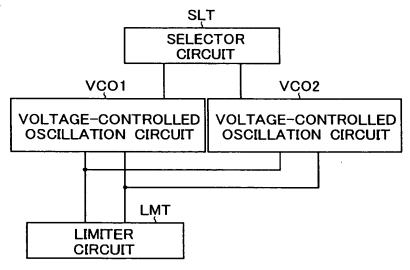


Fig. 5A VDD⊳ **⊲**GND VÇO -Ra Rb. VOLTAGE-CONTROLLED OSCILLATION CIRCUIT ia **⊲**GND' VDD'⊳ LMT ~Rc Rd-LIMITER īb **CIRCUIT** īb

Fig. 5B

VDDD

Ra

VCO

Rb

TIA+Ib

VOLTAGE-CONTROLLED

OSCILLATION CIRCUIT

Rd

Rd

Īb

LIMITER

CIRCUIT

īb

Fig. 6

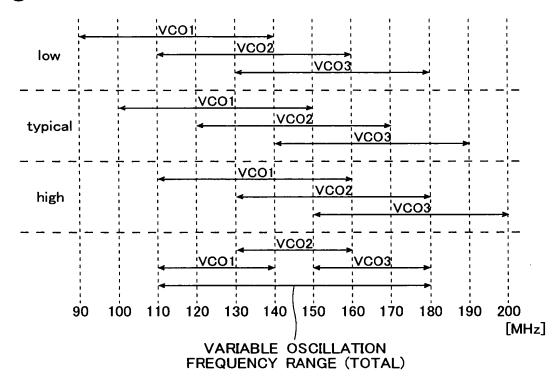


Fig. 7 Prior Art

